



CMOS/ 1.8V to 3.3V/ 2.0×1.6mm



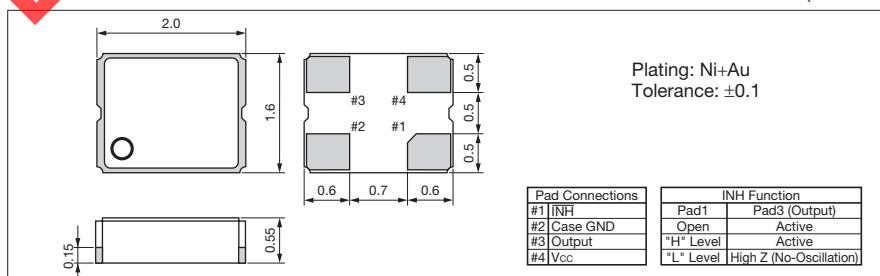
RoHS Compliant

Specifications

Item	Symbol	Conditions	Min.	Max.	Unit
Output Frequency Range	fo		-1.5	50	MHz
Frequency Tolerance	f_tol	Initial tolerance, Operating temperature range, Rated power supply voltage change, Aging (1 year @25°C), Shock and vibration	-100	+100	×10 ⁻⁶
		Temp.: -40 to +85°C	-50	+50	
		Temp.: -10 to +70°C / -40 to +85°C / -40 to +105°C	-30	+30	
		Temp.: -10 to +70°C	-25	+25	
Storage Temperature Range	T_stg		-55	+125	°C
Operating Temperature Range	T_use	Standard Specifications	-10	+70	°C
		Extend (Option)	-40	+85	
	Icc		-40	+105	mA
Max. Supply Voltage		—	-0.6	+6.0	
Supply Voltage	Vcc	1.5≤fo≤24MHz	—	2.5	
	24<fo≤40MHz	—	3.5		
	40<fo≤50MHz	—	4.5		
	1.5≤fo≤24MHz	—	3.0		
	24<fo≤40MHz	—	4.5		
	40<fo≤50MHz	—	5.0		
	1.5≤fo≤24MHz	—	3.5		
	24<fo≤40MHz	—	5.0		
	40<fo≤50MHz	—	6.0		
Stand-by Current	I_std		—	10	μA
Symmetry	SYM	@50% Vcc	45	55	%
Rise/ Fall Time (10% Vcc to 90% Vcc Maximum Loaded)	Tr/ Tf	1.0≤Vcc≤2.0V	—	6.5	ns
		2.0<Vcc≤2.8V	—	5.0	
		2.8<Vcc≤3.63V	—	4.5	
Low Level Output Voltage	VOL	IOL=4mA	—	10% Vcc	V
High Level Output Voltage	VOH	IOH=-4mA	90% Vcc	—	V
CMOS Load	L_Cmos	CMOS Output	—	15	pF
Input Voltage Range	VIN		0	Vcc	V
Low Level Input Voltage	VIL		—	30% Vcc	V
High Level Input Voltage	VIH		70% Vcc	—	V
Disable Time	t_dis		—	100	ns
Enable Time	t_ena		—	5	ms
Start-up Time	t_str	@Minimum operating voltage to be 0 sec.	—	10	ms
Sigma Jitter	JSigma	Measured with Wavecrest SIA-3000	—	8	ps
Peak to Peak Jitter	JPK-PK		—	80	ps

Note: All electrical characteristics are defined at the maximum load and operating temperature range.
Please contact us for inquiry about operating temperature range, available frequencies and other conditions.

Dimensions



Recommended Land Pattern

