

60 V, 7 A NPN low VCEsat transistor

20 September 2024

**Product data sheet** 

# 1. General description

NPN low  $V_{\text{CEsat}}$  transistor in a SOT223 (SC-73) medium power Surface-Mounted Device (SMD) plastic package.

PNP complement: PBSS4041PZ

# 2. Features and benefits

- Very low collector-emitter saturation voltage V<sub>CEsat</sub>
- High collector current capability  $I_C$  and  $I_{CM}$
- High collector current gain (h<sub>FE</sub>) at high I<sub>C</sub>
- High energy efficiency due to less heat generation
- · Smaller required Printed-Circuit Board (PCB) area than for conventional transistors
- AEC-Q101 qualified

### 3. Applications

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- Loadswitch
- Battery-driven devices
- Power management
- Charging circuits

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• Power switches (e.g. motors, fans)

### 4. Quick reference data

Table 1. Quick r	reference data					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>CEO</sub>	collector-emitter voltage	open base	-	-	60	V
I <sub>C</sub>	collector current		-	-	7	А
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms	-	-	15	А
R <sub>CEsat</sub>	collector-emitter saturation resistance	I <sub>C</sub> = 6 A; I <sub>B</sub> = 600 mA; pulsed; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C	-	17.5	25	mΩ

# nexperia

# 5. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	4	С
2	С	collector		
3	E	emitter		B — fx
4	С	collector		É
			SC-73 (SOT223)	sym123

# 6. Ordering information

Table 3. Ordering information					
Type number Package					
	Name	Description	Version		
PBSS4041NZ		plastic, surface-mounted package with increased heatsink; 4 leads; 2.3 mm pitch; 6.5 mm x 3.5 mm x 1.65 mm body	<u>SOT223</u>		

# 7. Marking

Table 4. Marking codes				
Type number	Marking code			
PBSS4041NZ	PB4041 NZ			

### 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

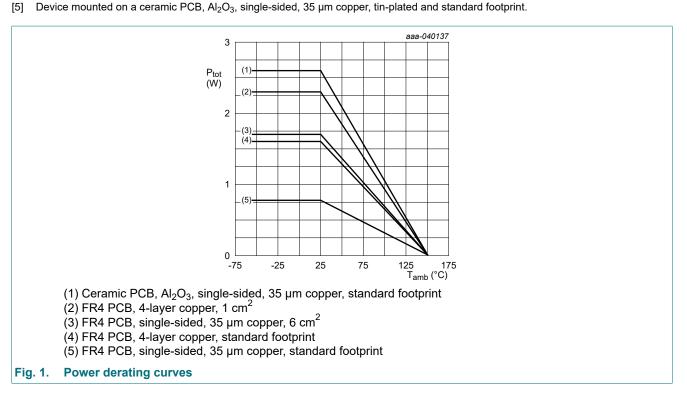
Symbol	Parameter	Conditions		Min	Мах	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter		-	60	V
V <sub>CEO</sub>	collector-emitter voltage	open base		-	60	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	5	V
I <sub>C</sub>	collector current			-	7	А
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms		-	15	А
I <sub>B</sub>	base current			-	1	А
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	0.77	W
			[2]	-	1.7	W
			[3]	-	1.6	W
			[4]	-	2.3	W
			[5]	-	2.6	W
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided, 35 µm copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided, 35 µm copper, tin-plated, mounting pad for collector 6 cm<sup>2</sup>.

[3] Device mounted on an FR4 PCB, 4-layer, tin-plated and standard footprint.

[4] Device mounted on an FR4 PCB, 4-layer, tin-plated, mounting pad for collector 1 cm<sup>2</sup>.



# 9. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
R <sub>th(j-a)</sub> thermal resistance from i junction to ambient		in free air	[1]	-	-	160	K/W
	junction to ambient	[2]	-	-	75	K/W	
	[3	[3]	-	-	80	K/W	
	[4]	[4]	-	-	55	K/W	
		[5]	-	-	50	K/W	
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point			-	-	11	K/W

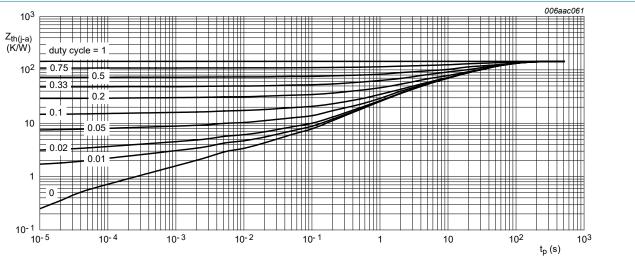
[1] Device mounted on an FR4 PCB, single-sided, 35 µm copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided, 35 µm copper, tin-plated, mounting pad for collector 6 cm<sup>2</sup>.

[3] Device mounted on an FR4 PCB, 4-layer, tin-plated and standard footprint.

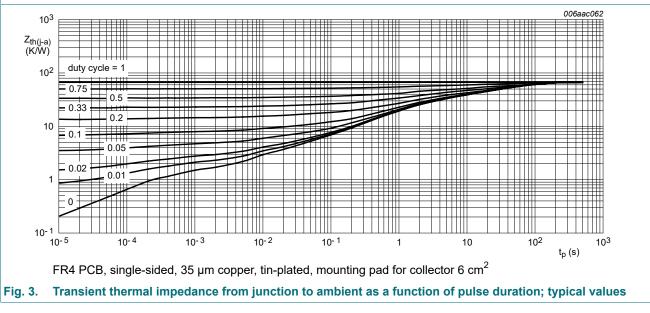
[4] Device mounted on an FR4 PCB, 4-layer, tin-plated, mounting pad for collector 1 cm<sup>2</sup>.

[5] Device mounted on a ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, single-sided, 35 μm copper, tin-plated and standard footprint.

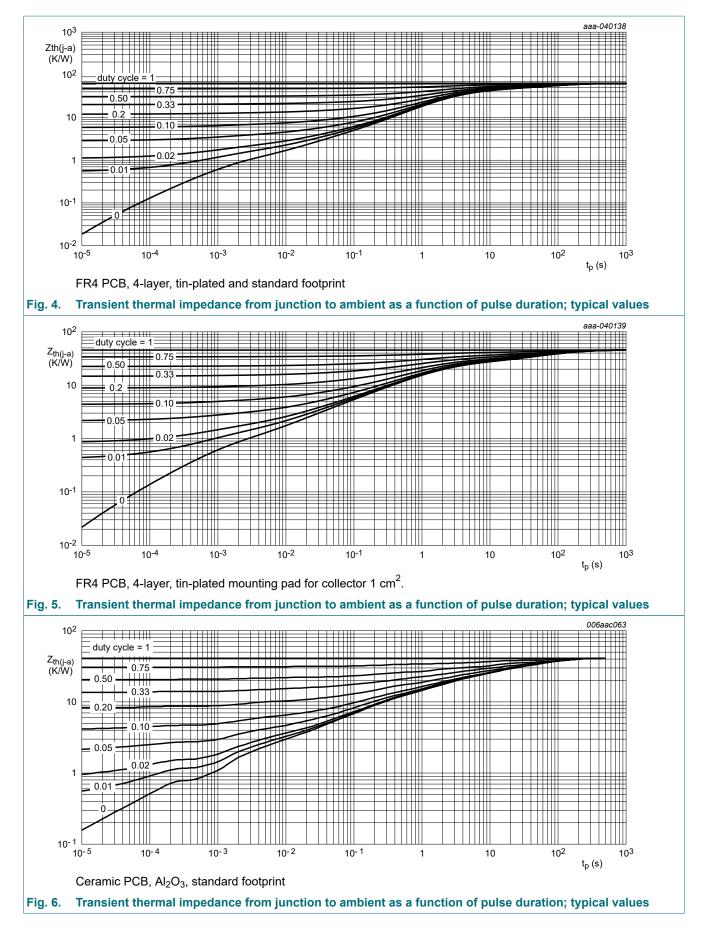


FR4 PCB, single-sided, 35 µm copper, tin-plated and standard footprint





#### 60 V, 7 A NPN low VCEsat transistor



PBSS4041NZ

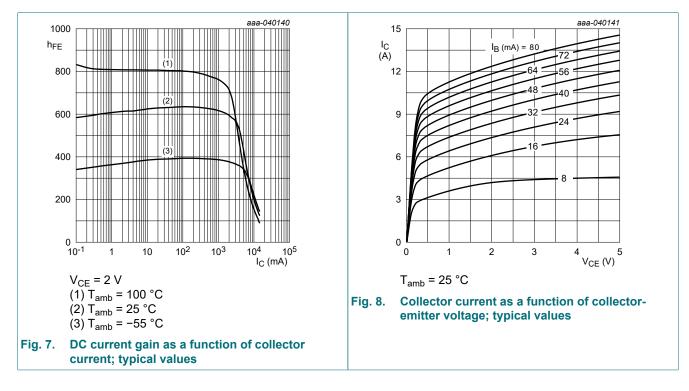
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# **10. Characteristics**

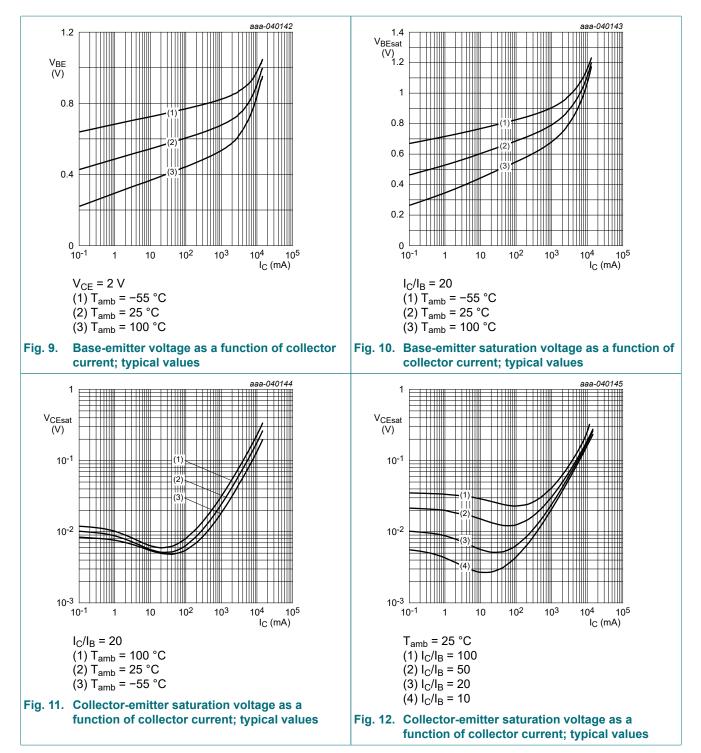
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>(BR)CBO</sub>	collector-base breakdown voltage	I <sub>C</sub> = 100 μA; I <sub>E</sub> = 0 A; T <sub>amb</sub> = 25 °C	60	-	-	V
V <sub>(BR)CEO</sub>	collector-emitter breakdown voltage	I <sub>C</sub> = 10 mA; I <sub>B</sub> = 0 A; T <sub>amb</sub> = 25 °C	60	-	-	V
V <sub>(BR)EBO</sub>	emitter-base breakdown voltage	$I_E$ = 100 µA; $I_C$ = 0 A; $T_{amb}$ = 25 °C	5	-	-	V
сво	collector-base cut-off	$V_{CB} = 60 \text{ V}; \text{ I}_{\text{E}} = 0 \text{ A}; \text{ T}_{\text{amb}} = 25 ^{\circ}\text{C}$	-	-	100	nA
	current	V <sub>CB</sub> = 60 V; I <sub>E</sub> = 0 A; T <sub>j</sub> = 150 °C	-	-	50	μA
CES	collector-emitter cut-off current	V <sub>CE</sub> = 48 V; V <sub>BE</sub> = 0 V; T <sub>amb</sub> = 25 °C	-	-	100	nA
EBO	emitter-base cut-off current	V <sub>EB</sub> = 5 V; I <sub>C</sub> = 0 A; T <sub>amb</sub> = 25 °C	-	-	100	nA
η <sub>FE</sub>	DC current gain	V <sub>CE</sub> = 2 V; I <sub>C</sub> = 500 mA; pulsed; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C	300	530	-	
		$V_{CE}$ = 2 V; I <sub>C</sub> = 1 A; pulsed; t <sub>p</sub> ≤ 300 µs; $\delta$ ≤ 0.02; T <sub>amb</sub> = 25 °C	300	520	-	
		$ \begin{array}{l} V_{CE} \texttt{= 2 V; } I_{C} \texttt{= 2 A; pulsed; } t_{p} \texttt{\leq } 300 \ \texttt{\mu s;} \\ \delta \texttt{\leq } 0.02; \ T_{amb} \texttt{= 25 °C} \end{array} $	300	500	-	
		$V_{CE}$ = 2 V; I <sub>C</sub> = 4 A; pulsed; t <sub>p</sub> ≤ 300 µs; $\delta$ ≤ 0.02; T <sub>amb</sub> = 25 °C	250	430	-	
		$V_{CE}$ = 2 V; I <sub>C</sub> = 6 A; pulsed; t <sub>p</sub> ≤ 300 µs; $\delta$ ≤ 0.02; T <sub>amb</sub> = 25 °C	100	320	-	
		$V_{CE}$ = 2 V; I <sub>C</sub> = 7 A; pulsed; t <sub>p</sub> ≤ 300 µs; $\delta$ ≤ 0.02; T <sub>amb</sub> = 25 °C	50	240	-	
V <sub>CEsat</sub>	collector-emitter saturation voltage	I <sub>C</sub> = 1 A; I <sub>B</sub> = 10 mA; pulsed; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C	-	45	60	mV
		$I_C$ = 1 A; $I_B$ = 50 mA; pulsed; $t_p$ ≤ 300 μs; δ ≤ 0.02; $T_{amb}$ = 25 °C	-	25	35	mV
		I <sub>C</sub> = 2 A; I <sub>B</sub> = 40 mA; pulsed; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C	-	55	75	mV
		$I_C = 4$ A; $I_B = 40$ mA; pulsed; $t_p \le 300$ μs; δ $\le 0.02$ ; $T_{amb} = 25$ °C	-	115	160	mV
		I <sub>C</sub> = 4 A; I <sub>B</sub> = 200 mA; pulsed; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C	-	75	110	mV
		I <sub>C</sub> = 7 A; I <sub>B</sub> = 350 mA; pulsed; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C	-	125	195	mV
R <sub>CEsat</sub>	collector-emitter saturation resistance	I <sub>C</sub> = 6 A; I <sub>B</sub> = 600 mA; pulsed; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C	-	17.5	25	mΩ
V <sub>BEsat</sub>	base-emitter saturation voltage	I <sub>C</sub> = 1 A; I <sub>B</sub> = 100 mA; pulsed; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C	-	0.82	0.9	V
		I <sub>C</sub> = 4 A; I <sub>B</sub> = 400 mA; pulsed; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C	-	0.97	1.05	V
V <sub>BE</sub>	base-emitter voltage	$V_{CE}$ = 2 V; I <sub>C</sub> = 2 A; pulsed; t <sub>p</sub> ≤ 300 µs; $\delta \le 0.02$ ; T <sub>amb</sub> = 25 °C	-	0.72	0.85	V

#### 60 V, 7 A NPN low VCEsat transistor

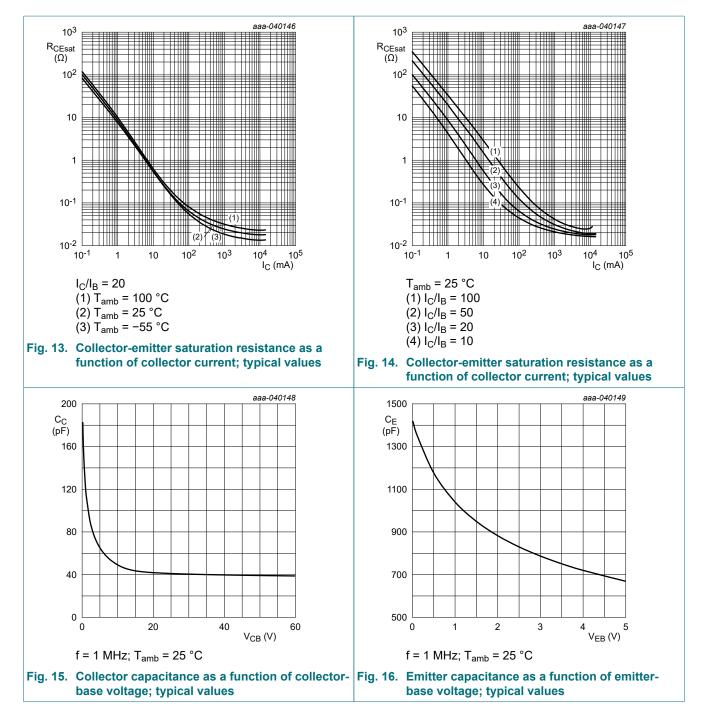
Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
t <sub>d</sub>	delay time	V <sub>CC</sub> = 12.5 V; I <sub>C</sub> = 1 A; I <sub>Bon</sub> = 50 mA;	-	65	-	ns
t <sub>r</sub>	rise time	I <sub>Boff</sub> = -50 mA; T <sub>amb</sub> = 25 °C	-	40	-	ns
t <sub>on</sub>	turn-on time		-	105	-	ns
ts	storage time	_	-	1125	-	ns
t <sub>f</sub>	fall time		-	95	-	ns
t <sub>off</sub>	turn-off time		-	1220	-	ns
f <sub>T</sub>	transition frequency	$V_{CE}$ = 10 V; I <sub>C</sub> = 100 mA; f = 100 MHz; T <sub>amb</sub> = 25 °C	-	100	-	MHz
C <sub>c</sub>	collector capacitance	$V_{CB}$ = 10 V; I <sub>E</sub> = 0 A; i <sub>e</sub> = 0 A; f = 1 MHz; T <sub>amb</sub> = 25 °C	-	50	-	pF



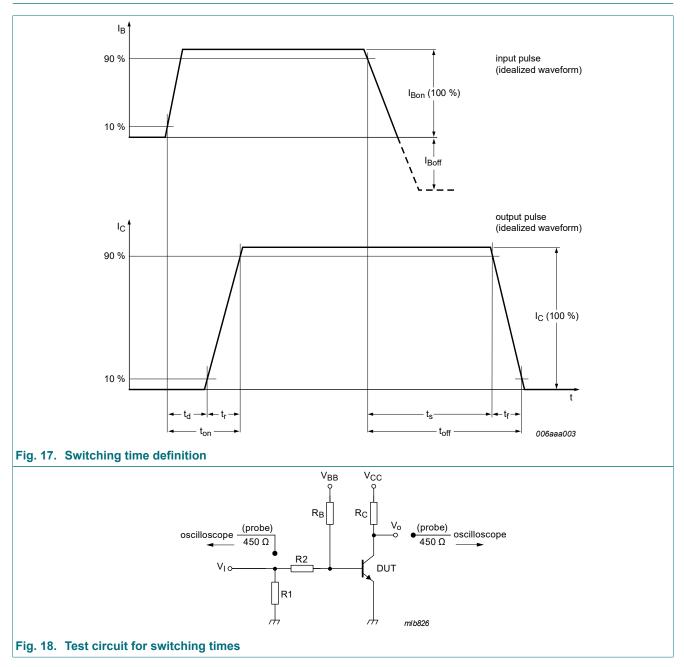
#### 60 V, 7 A NPN low VCEsat transistor



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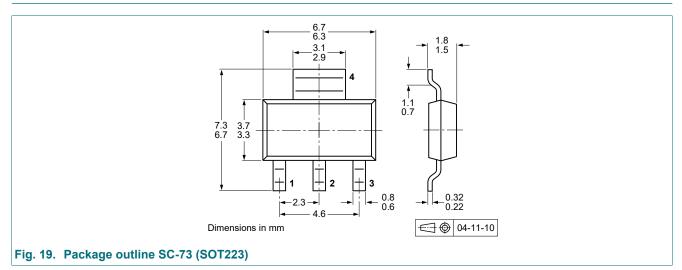
# **11. Test information**



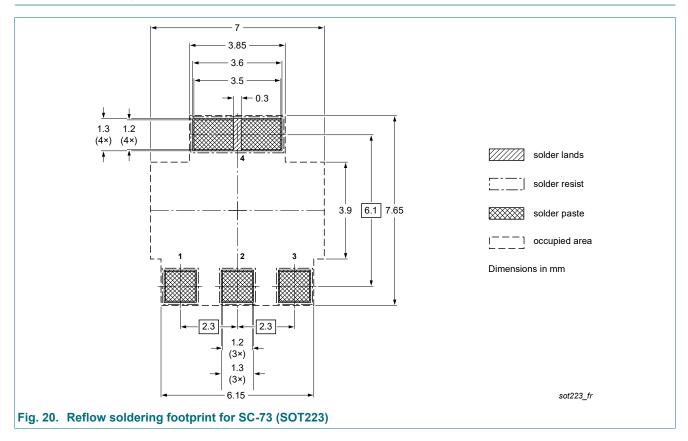
#### **Quality information**

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

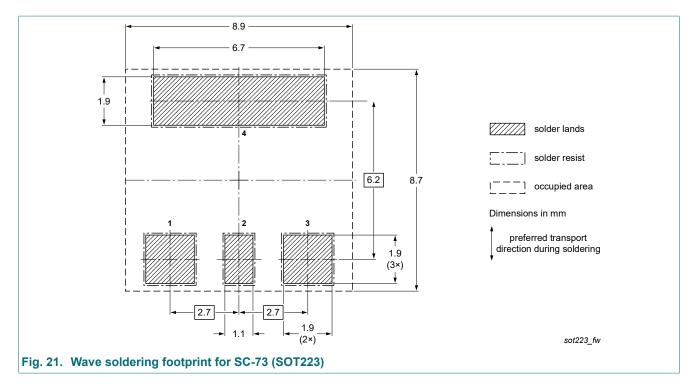
# 12. Package outline



### 13. Soldering



#### 60 V, 7 A NPN low VCEsat transistor



# 14. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PBSS4041NZ v.3	20240920	Product data sheet	-	PBSS4041NZ v.2
Modifications:	New graphics a	dded, graphs updated and v	alues changed.	
PBSS4041NZ v.2	0120808	Product data sheet	-	PBSS4041NZ v.1
PBSS4041NZ v.1	20100331	Product data sheet	-	-

# 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

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# Contents

1. General description	1
2. Features and benefits	1
3. Applications	1
4. Quick reference data	1
5. Pinning information	2
6. Ordering information	2
7. Marking	2
8. Limiting values	3
9. Thermal characteristics	4
10. Characteristics	6
11. Test information	10
12. Package outline	11
13. Soldering	
14. Revision history	13
15. Legal information	
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