

PNP Epitaxial Silicon Transistor

KSP92

Description

High Voltage Transistor

Features

- These Devices are Pb-Free, Halogen Free/BFR Free, Beryllium Free and are RoHS Compliant

ABSOLUTE MAXIMUM RATINGS (T_a = 25°C, unless otherwise noted)

Symbol	Parameter	Value	Unit
V _{CBO}	Collector-Base Voltage	-300	V
V _{CEO}	Collector-Emitter Voltage	-300	V
V _{EBO}	Emitter-Base Voltage	-5	V
I _C	Collector Current	-500	mA
P _C	Collector Power Dissipation (T _a = 25°C)	625	mW
	Derate above 25°C	5	mW/°C
P _C	Collector Power Dissipation (T _C = 25°C)	1.5	W
	Derate above 25°C	12	mW/°C
T _J	Junction Temperature	150	°C
T _{STG}	Storage Temperature	-55-150	°C

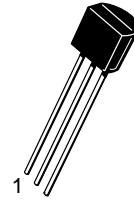
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

ELECTRICAL CHARACTERISTICS (T_a = 25°C unless otherwise noted)

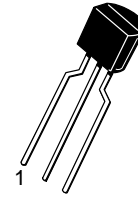
Symbol	Parameter	Test Condition	Min	Max	Unit
BV _{CBO}	Collector-Base Breakdown Voltage	I _C = -100 μA, I _E = 0	-300	-	V
BV _{CEO}	* Collector-Emitter Breakdown Voltage	I _C = -1 mA, I _B = 0	-300	-	V
BV _{EBO}	Emitter-Base Breakdown Voltage	I _E = -100 μA, I _C = 0	-5	-	V
I _{CBO}	Collector Cur-off Current	V _{CB} = -200 V, I _E = 0	-	-0.25	μA
I _{EBO}	Emitter Cut-off Current	V _{EB} = -3 V, I _C = 0	-	-0.10	μA
h _{FE}	* DC Current Gain	V _{CE} = -10 V, I _C = -1 mA V _{CE} = -10 V, I _C = -10 mA V _{CE} = -10 V, I _C = -30 mA	25 40 25	-	
V _{CE} (sat)	*Collector-Emitter Saturation Voltage	I _C = -20 mA, I _B = -2 mA	-	-0.50	V
V _{BE} (sat)	* Base-Emitter Saturation Voltage	I _C = -20 mA, I _B = -2 mA	-	-0.90	V
f _T	Current Gain Bandwidth Product	V _{CE} = -20 V, I _C = -10 mA, f = 100 MHz	50	-	MHz
C _{ob}	Output Capacitance	V _{CB} = -20 V, I _E = 0, f = 1 MHz	-	6	pF

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

*Pulse Test: PW ≤ 300 μs, Duty Cycle ≤ 2%.



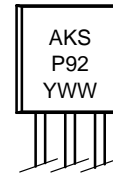
TO-92 3
4.825x4.76
CASE 135AN



TO-92 3
4.83x4.76
LEADFORMED
CASE 135AR

1. Emitter
2. Base
3. Collector

MARKING DIAGRAM



KSP92 = Specific Device Code
A = Assembly Site
WW = Work Week Number
Y = Year of Production

ORDERING INFORMATION

Device	Package	Packing Method
KSP92BU	TO-92 3, CASE 135AN	10000 Units / Bulk Bag
KSP92TA	TO-92 3, CASE 135AR	2000 Units / Fan-Fold

TYPICAL PERFORMANCE CHARACTERISTICS

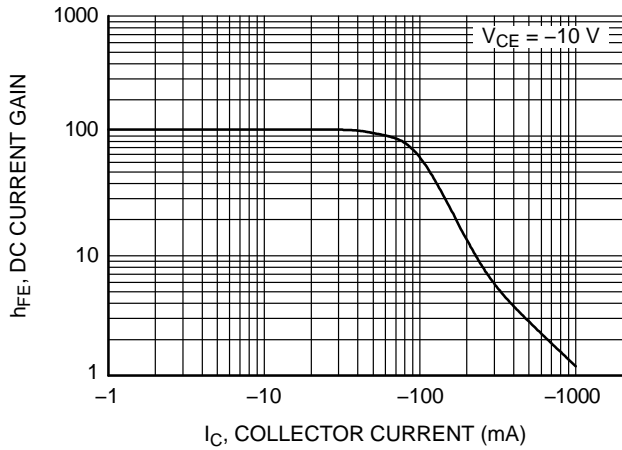


Figure 1. DC Current Gain

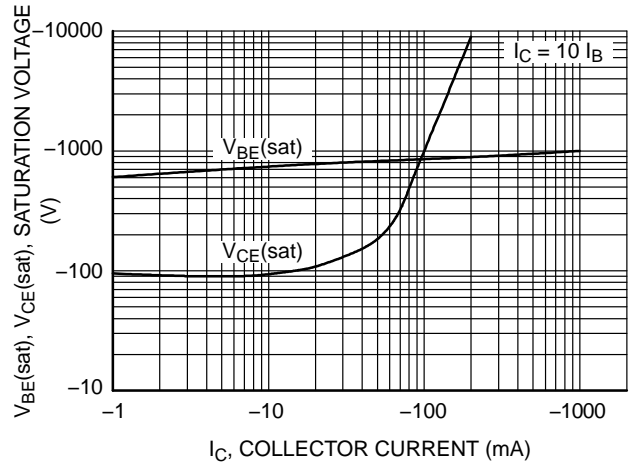


Figure 2. Saturation Voltage

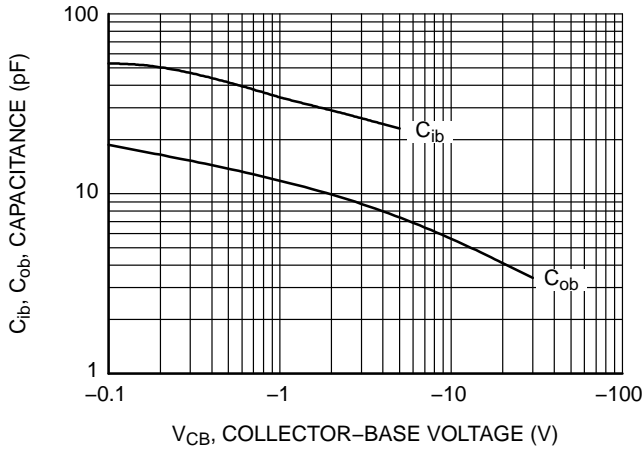


Figure 3. Capacitance

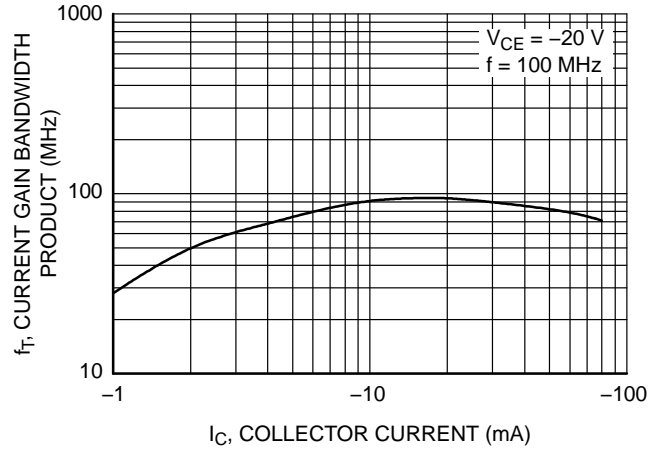


Figure 4. Current Gain Bandwidth Product

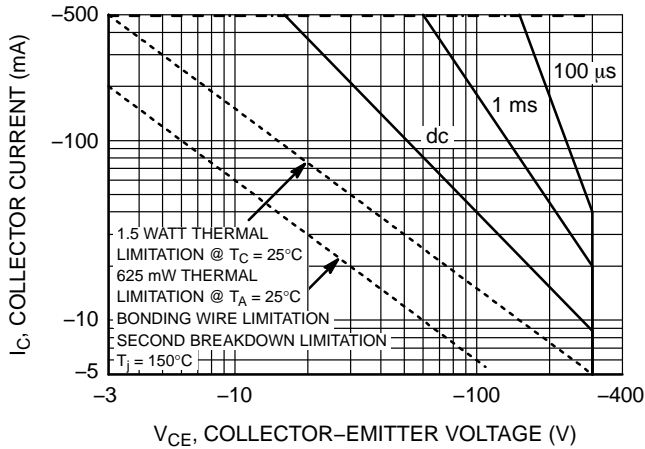
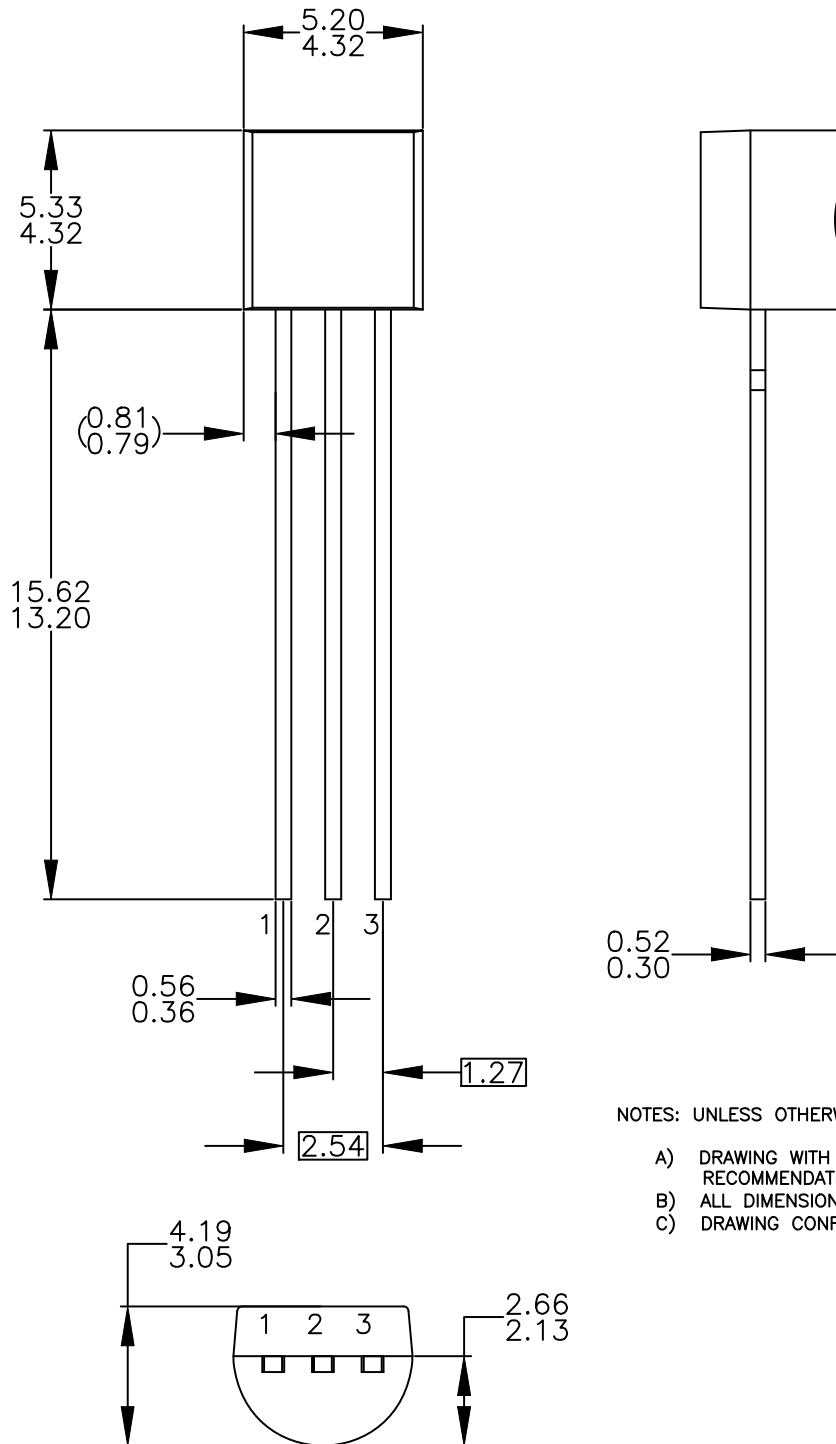


Figure 5. Active-Region Safe Operating Area

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DATE 31 JUL 2016



NOTES: UNLESS OTHERWISE SPECIFIED

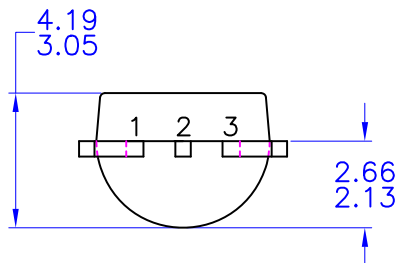
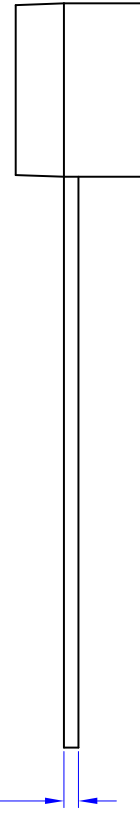
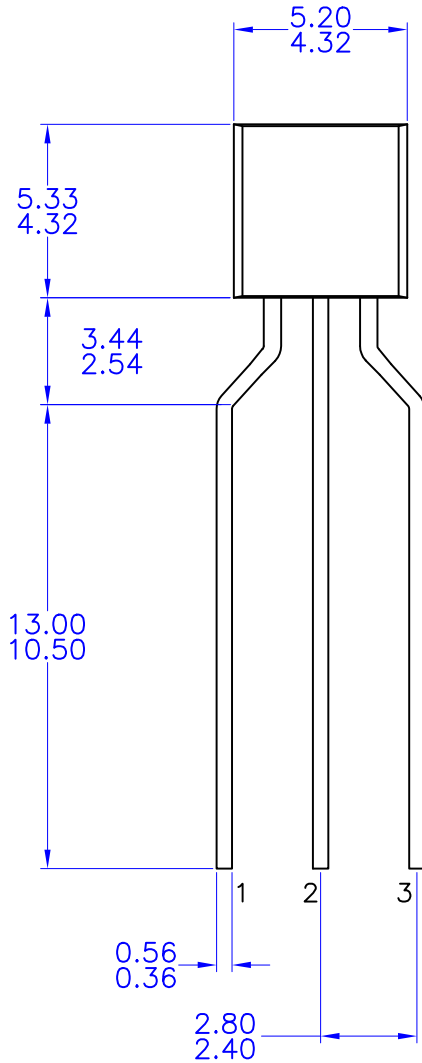
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