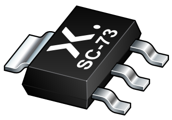


PBSS4021PZ-Q

20 V, 6.6 A PNP low VCEsat transistor

20 September 2024 — Product data sheet

1. General description

PNP low V_{CEsat} transistor in a SOT223 (SC-73) medium power Surface-Mounted Device (SMD) plastic package.

NPN complement: PBSS4021NZ-Q

2. Features and benefits

- Very low collector-emitter saturation voltage V_{CEsat}
- High collector current capability I_C and I_{CM}
- High collector current gain (h_{FE}) at high I_C
- High energy efficiency due to less heat generation
- Smaller required Printed-Circuit Board (PCB) area than for conventional transistors
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

- Loadswitch
- Battery-driven devices
- Power management
- Charging circuits
- Power switches (e.g. motors, fans)

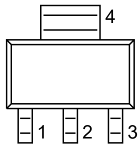
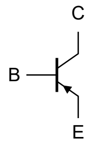
4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
V_{CEO}	collector-emitter voltage	open base		-	-	-20	V
I_C	collector current			-	-	-6.6	A
I_{CM}	peak collector current	single pulse; $t_p \leq$ 1 ms		-	-	-20	A
R_{CEsat}	collector-emitter saturation resistance	I_C = -6 A; I_B = -600 mA; pulsed; $t_p \leq$ 300 µs; $\delta \leq$ 0.02; T_{amb} = 25 °C		-	22	33	mΩ

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	B	base	 **SC-73 (SOT223)**	 *sym028*
2	C	collector		
3	E	emitter		
4	C	collector		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PBSS4021PZ-Q	SC-73	plastic, surface-mounted package with increased heatsink; 4 leads; 2.3 mm pitch; 6.5 mm x 3.5 mm x 1.65 mm body	SOT223

7. Marking

Table 4. Marking codes

Type number	Marking code
PBSS4021PZ-Q	PB4021 PZ

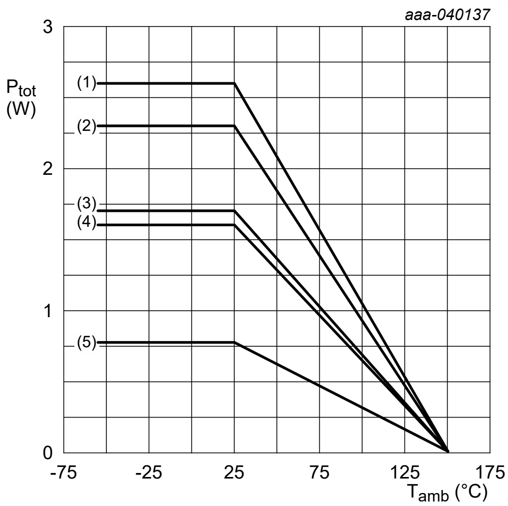
8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{CBO}	collector-base voltage	open emitter		-	-20	V
V_{CEO}	collector-emitter voltage	open base		-	-20	V
V_{EBO}	emitter-base voltage	open collector		-	-5	V
I_C	collector current			-	-6.6	A
I_{CM}	peak collector current	single pulse; $t_p \leq$ 1 ms		-	-20	A
I_B	base current			-	-1	A
P_{tot}	total power dissipation	$T_{amb} \leq$ 25 °C	[1]	-	0.77	W
			[2]	-	1.7	W
			[3]	-	1.6	W
			[4]	-	2.3	W
			[5]	-	2.6	W
T_j	junction temperature			-	150	°C
T_{amb}	ambient temperature			-55	150	°C
T_{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided, 35 µm copper, tin-plated and standard footprint.
[2] Device mounted on an FR4 PCB, single-sided, 35 µm copper, tin-plated, mounting pad for collector 6 cm^2.
[3] Device mounted on an FR4 PCB, 4-layer, tin-plated and standard footprint.
[4] Device mounted on an FR4 PCB, 4-layer, tin-plated, mounting pad for collector 1 cm^2.
[5] Device mounted on a ceramic PCB, Al_2O_3, single-sided, 35 µm copper, tin-plated and standard footprint.



(1) Ceramic PCB, Al_2O_3, single-sided, 35 µm copper, standard footprint
(2) FR4 PCB, 4-layer copper, 1 cm^2
(3) FR4 PCB, single-sided, 35 µm copper, 6 cm^2
(4) FR4 PCB, 4-layer copper, standard footprint
(5) FR4 PCB, single-sided, 35 µm copper, standard footprint

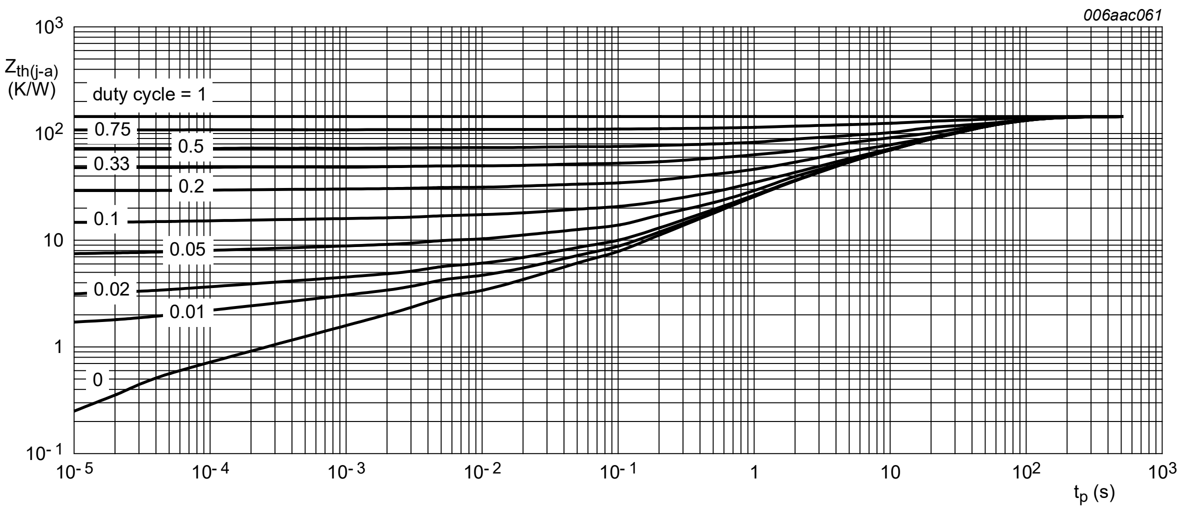
Fig. 1. Power derating curves

9. Thermal characteristics

Table 6. Thermal characteristics

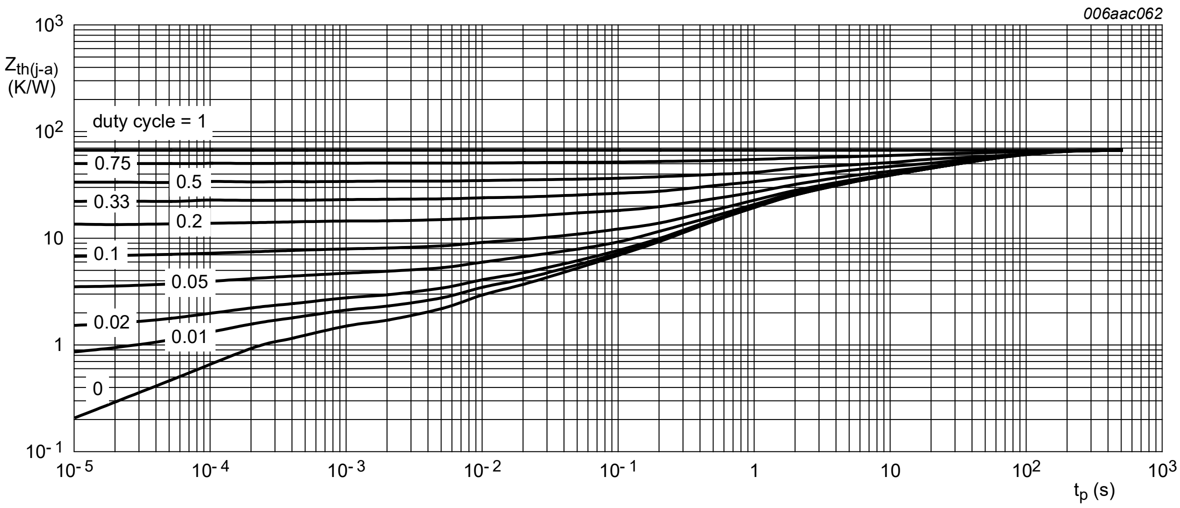
Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	160	K/W
			[2]	-	-	75	K/W
			[3]	-	-	80	K/W
			[4]	-	-	55	K/W
			[5]	-	-	50	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point			-	-	11	K/W

[1] Device mounted on an FR4 PCB, single-sided, 35 µm copper, tin-plated and standard footprint.
[2] Device mounted on an FR4 PCB, single-sided, 35 µm copper, tin-plated, mounting pad for collector 6 cm^2.
[3] Device mounted on an FR4 PCB, 4-layer, tin-plated and standard footprint.
[4] Device mounted on an FR4 PCB, 4-layer, tin-plated, mounting pad for collector 1 cm^2.
[5] Device mounted on a ceramic PCB, Al_2O_3, single-sided, 35 µm copper, tin-plated and standard footprint.



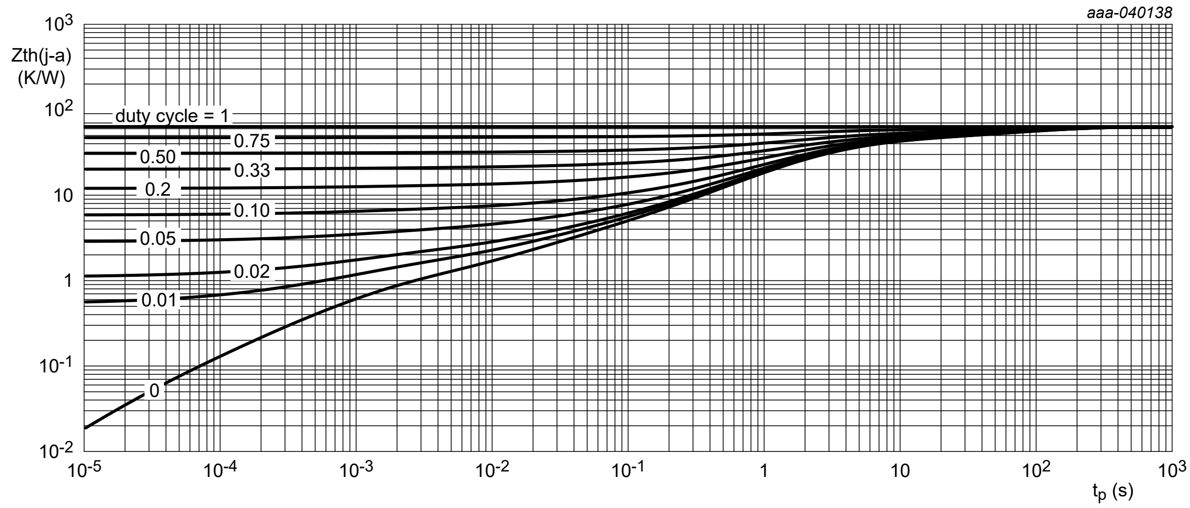
FR4 PCB, single-sided, 35 µm copper, tin-plated and standard footprint

Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



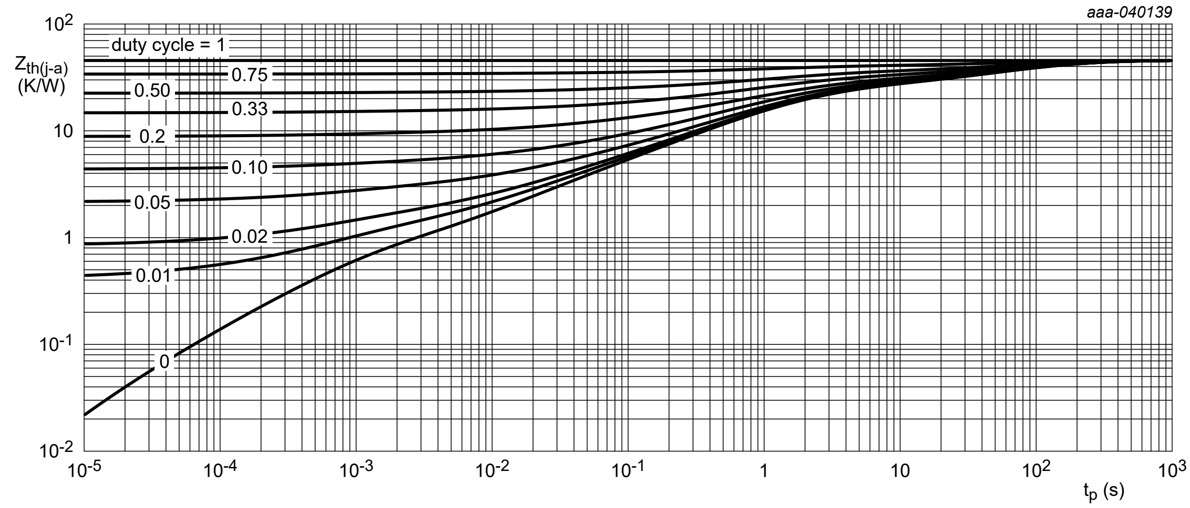
FR4 PCB, single-sided, 35 µm copper, tin-plated, mounting pad for collector 6 cm^2

Fig. 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



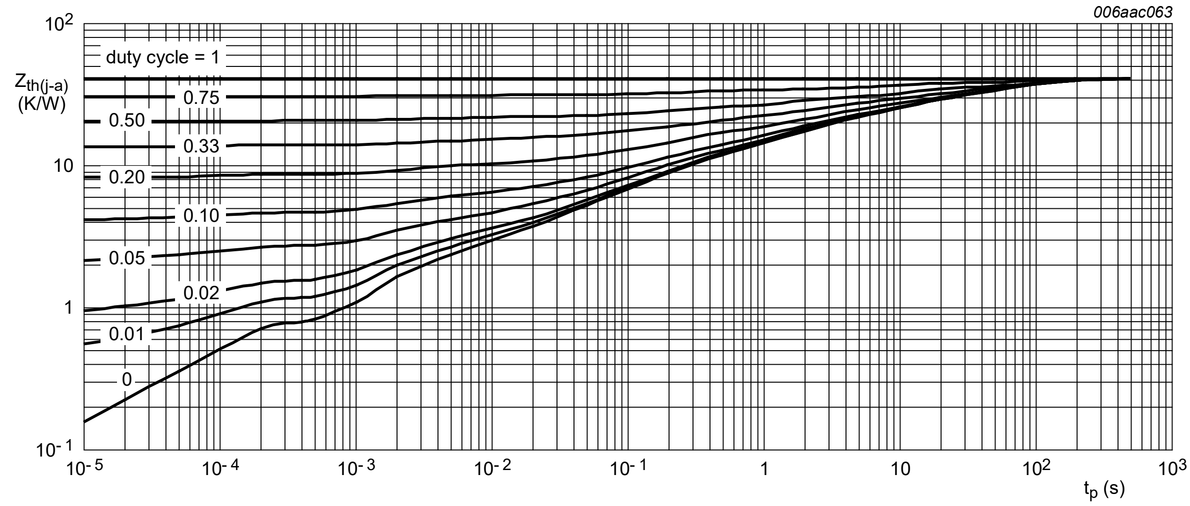
FR4 PCB, 4-layer, tin-plated and standard footprint

Fig. 4. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB, 4-layer, tin-plated mounting pad for collector 1 cm^2.

Fig. 5. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



Ceramic PCB, Al_2O_3, standard footprint

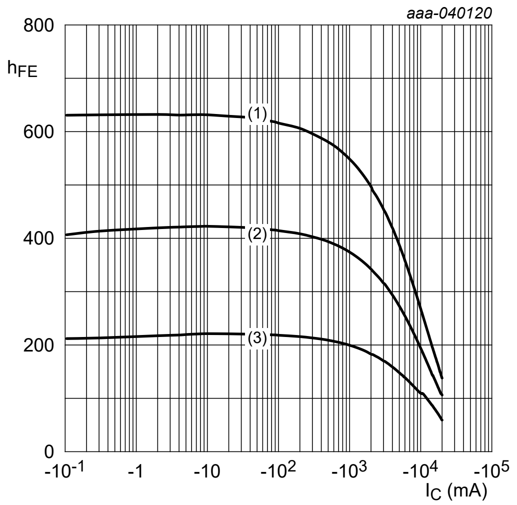
Fig. 6. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

10. Characteristics

Table 7. Characteristics

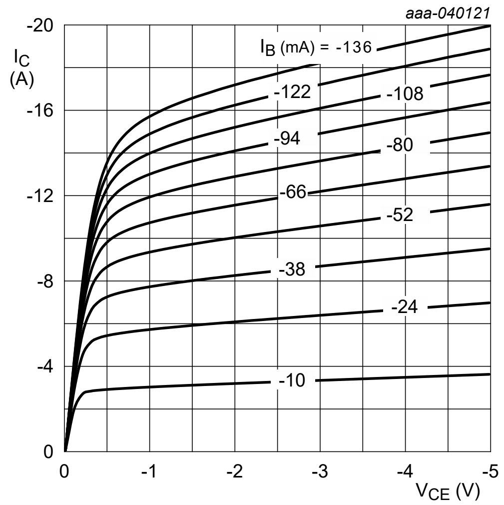
Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$V_{(BR)CBO}$	collector-base breakdown voltage	I_C = -100 µA; I_E = 0 A; T_{amb} = 25 °C		-20	-	-	V
$V_{(BR)CEO}$	collector-emitter breakdown voltage	I_C = -10 mA; I_B = 0 A; T_{amb} = 25 °C		-20	-	-	V
$V_{(BR)EBO}$	emitter-base breakdown voltage	I_E = -100 µA; I_C = 0 A; T_{amb} = 25 °C		-5	-	-	V
I_{CBO}	collector-base cut-off current	V_{CB} = -20 V; I_E = 0 A; T_{amb} = 25 °C		-	-	-100	nA
		V_{CB} = -20 V; I_E = 0 A; T_j = 150 °C		-	-	-55	µA
I_{CES}	collector-emitter cut-off current	V_{CE} = -16 V; V_{BE} = 0 V; T_{amb} = 25 °C		-	-	-100	nA
I_{EBO}	emitter-base cut-off current	V_{EB} = -5 V; I_C = 0 A; T_{amb} = 25 °C		-	-	-100	nA
h_{FE}	DC current gain	V_{CE} = -2 V; I_C = -500 mA; pulsed; t_p ≤ 300 µs; δ ≤ 0.02; T_{amb} = 25 °C		250	390	-	
		V_{CE} = -2 V; I_C = -1 A; pulsed; t_p ≤ 300 µs; δ ≤ 0.02; T_{amb} = 25 °C		250	370	-	
		V_{CE} = -2 V; I_C = -2 A; pulsed; t_p ≤ 300 µs; δ ≤ 0.02; T_{amb} = 25 °C		200	340	-	
		V_{CE} = -2 V; I_C = -4 A; pulsed; t_p ≤ 300 µs; δ ≤ 0.02; T_{amb} = 25 °C		150	310	-	
		V_{CE} = -2 V; I_C = -7 A; pulsed; t_p ≤ 300 µs; δ ≤ 0.02; T_{amb} = 25 °C		100	210	-	
V_{CEsat}	collector-emitter saturation voltage	I_C = -1 A; I_B = -10 mA; pulsed; t_p ≤ 300 µs; δ ≤ 0.02; T_{amb} = 25 °C		-	-50	-80	mV
		I_C = -1 A; I_B = -50 mA; pulsed; t_p ≤ 300 µs; δ ≤ 0.02; T_{amb} = 25 °C		-	-30	-50	mV
		I_C = -2 A; I_B = -40 mA; pulsed; t_p ≤ 300 µs; δ ≤ 0.02; T_{amb} = 25 °C		-	-60	-100	mV
		I_C = -4 A; I_B = -40 mA; pulsed; t_p ≤ 300 µs; δ ≤ 0.02; T_{amb} = 25 °C		-	-120	-225	mV
		I_C = -4 A; I_B = -200 mA; pulsed; t_p ≤ 300 µs; δ ≤ 0.02; T_{amb} = 25 °C		-	-85	-140	mV
		I_C = -7 A; I_B = -350 mA; pulsed; t_p ≤ 300 µs; δ ≤ 0.02; T_{amb} = 25 °C		-	-140	-240	mV
R_{CEsat}	collector-emitter saturation resistance	I_C = -6 A; I_B = -600 mA; pulsed; t_p ≤ 300 µs; δ ≤ 0.02; T_{amb} = 25 °C		-	22	33	mΩ
V_{BEsat}	base-emitter saturation voltage	I_C = -1 A; I_B = -100 mA; pulsed; t_p ≤ 300 µs; δ ≤ 0.02; T_{amb} = 25 °C		-	-0.8	-0.9	V
		I_C = -4 A; I_B = -400 mA; pulsed; t_p ≤ 300 µs; δ ≤ 0.02; T_{amb} = 25 °C		-	-0.92	-1.05	V
V_{BE}	base-emitter voltage	V_{CE} = -2 V; I_C = -2 A; pulsed; t_p ≤ 300 µs; δ ≤ 0.02; T_{amb} = 25 °C		-	-0.75	-0.85	V

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
t_d	delay time	V_{CC} = -12.5 V; I_C = -1 A; I_{Bon} = -50 mA; I_{Boff} = 50 mA; T_{amb} = 25 °C		-	70	-	ns
t_r	rise time			-	70	-	ns
t_{on}	turn-on time			-	140	-	ns
t_s	storage time			-	380	-	ns
t_f	fall time			-	80	-	ns
t_{off}	turn-off time			-	460	-	ns
f_T	transition frequency	V_{CE} = -10 V; I_C = -100 mA; f = 100 MHz; T_{amb} = 25 °C		-	74	-	MHz
C_c	collector capacitance	V_{CB} = -10 V; I_E = 0 A; i_e = 0 A; f = 1 MHz; T_{amb} = 25 °C		-	180	-	pF



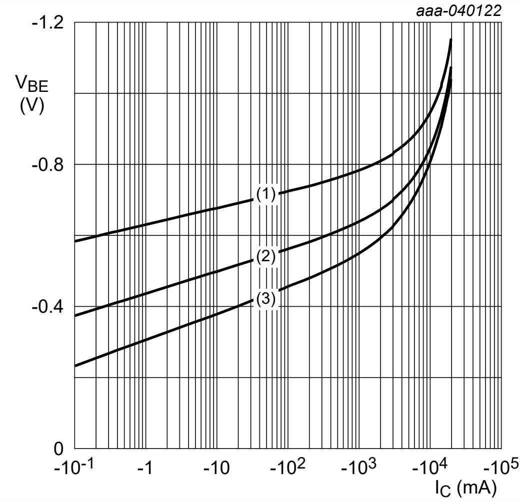
V_{CE} = −2 V
(1) T_{amb} = 100 °C
(2) T_{amb} = 25 °C
(3) T_{amb} = −55 °C

Fig. 7. DC current gain as a function of collector current; typical values



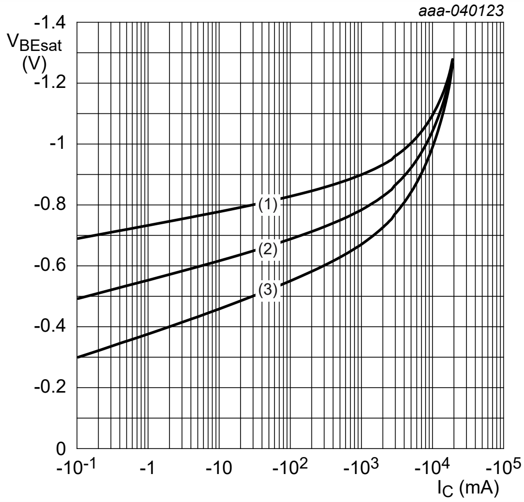
T_{amb} = 25 °C

Fig. 8. Collector current as a function of collector-emitter voltage; typical values



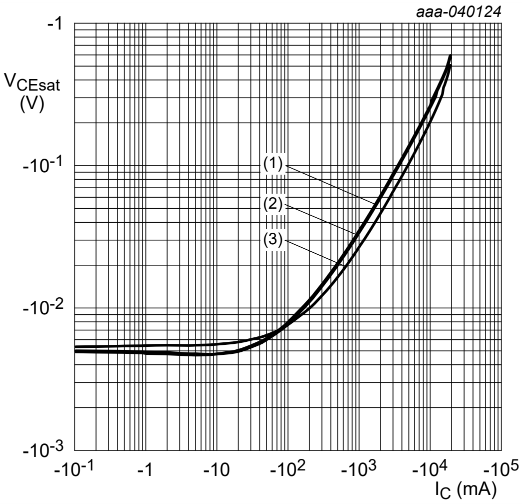
V_{CE} = −2 V
(1) T_{amb} = −55 °C
(2) T_{amb} = 25 °C
(3) T_{amb} = 100 °C

Fig. 9. Base-emitter voltage as a function of collector current; typical values



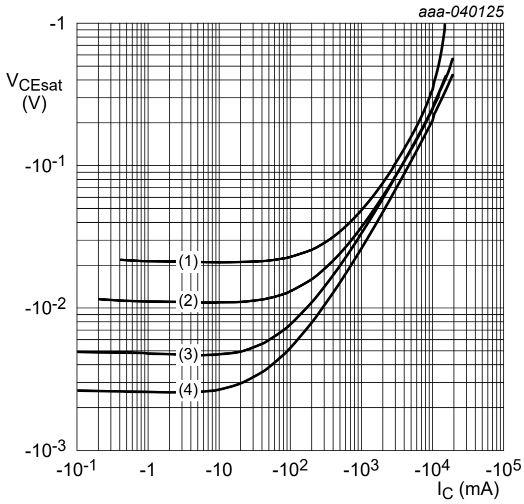
I_C/I_B = 20
(1) T_{amb} = −55 °C
(2) T_{amb} = 25 °C
(3) T_{amb} = 100 °C

Fig. 10. Base-emitter saturation voltage as a function of collector current; typical values



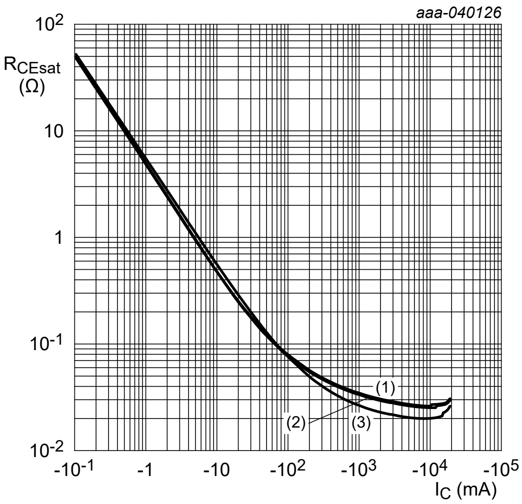
I_C/I_B = 20
(1) T_{amb} = 100 °C
(2) T_{amb} = 25 °C
(3) T_{amb} = −55 °C

Fig. 11. Collector-emitter saturation voltage as a function of collector current; typical values



T_{amb} = 25 °C
(1) I_C/I_B = 100
(2) I_C/I_B = 50
(3) I_C/I_B = 20
(4) I_C/I_B = 10

Fig. 12. Collector-emitter saturation voltage as a function of collector current; typical values



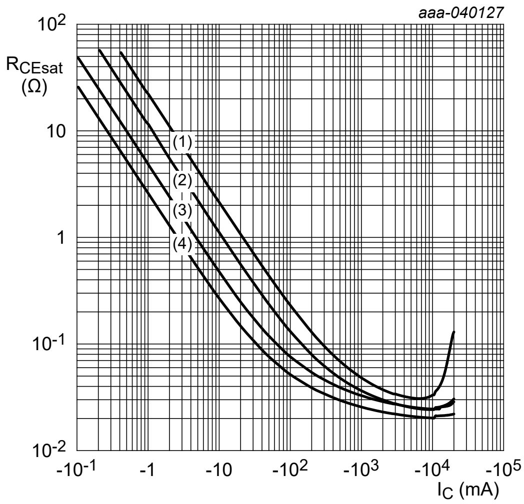
$I_C/I_B = 20$

(1) $T_{amb} = 100\ °C$

(2) $T_{amb} = 25\ °C$

(3) $T_{amb} = -55\ °C$

Fig. 13. Collector-emitter saturation resistance as a function of collector current; typical values



$T_{amb} = 25\ °C$

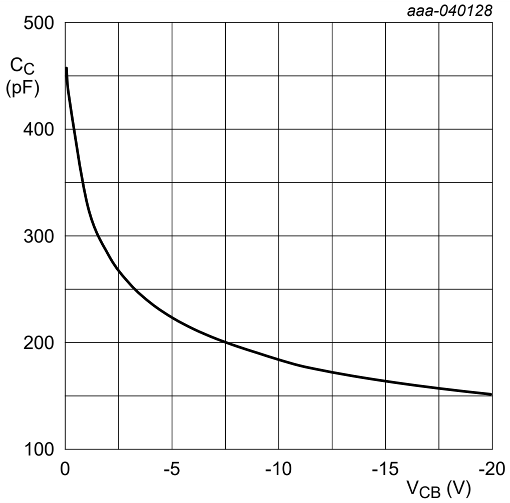
(1) $I_C/I_B = 100$

(2) $I_C/I_B = 50$

(3) $I_C/I_B = 20$

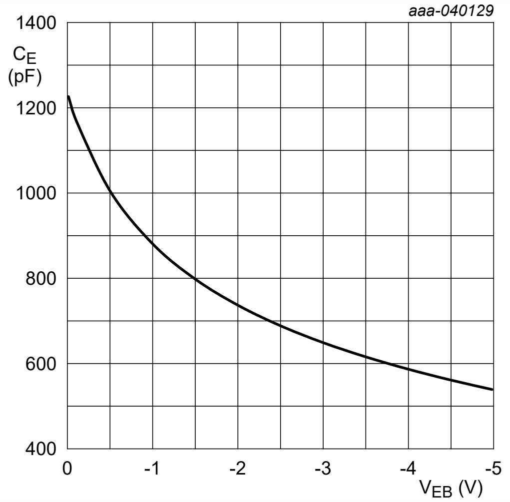
(4) $I_C/I_B = 10$

Fig. 14. Collector-emitter saturation resistance as a function of collector current; typical values



f = 1 MHz; $T_{amb} = 25\ °C$

Fig. 15. Collector capacitance as a function of collector-base voltage; typical values



f = 1 MHz; $T_{amb} = 25\ °C$

Fig. 16. Emitter capacitance as a function of emitter-base voltage; typical values

11. Test information

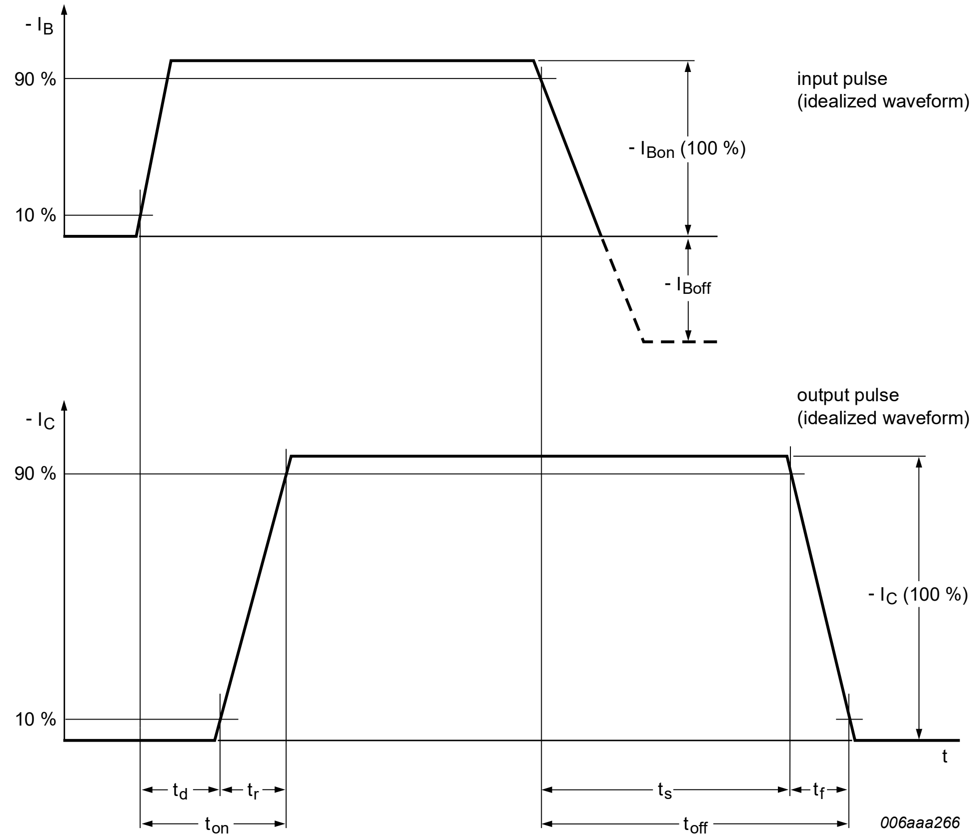


Fig. 17. Transistor switching time definition

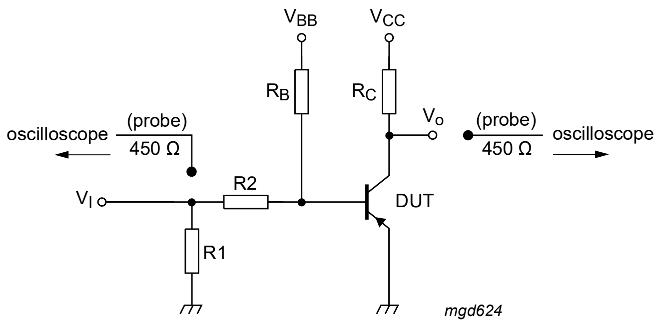


Fig. 18. Test circuit for switching times

Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline

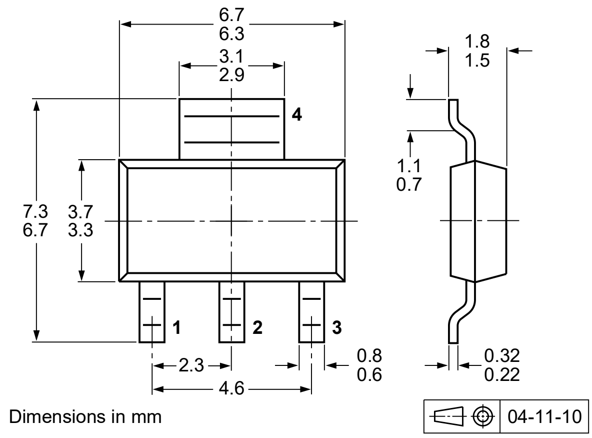


Fig. 19. Package outline SC-73 (SOT223)

13. Soldering

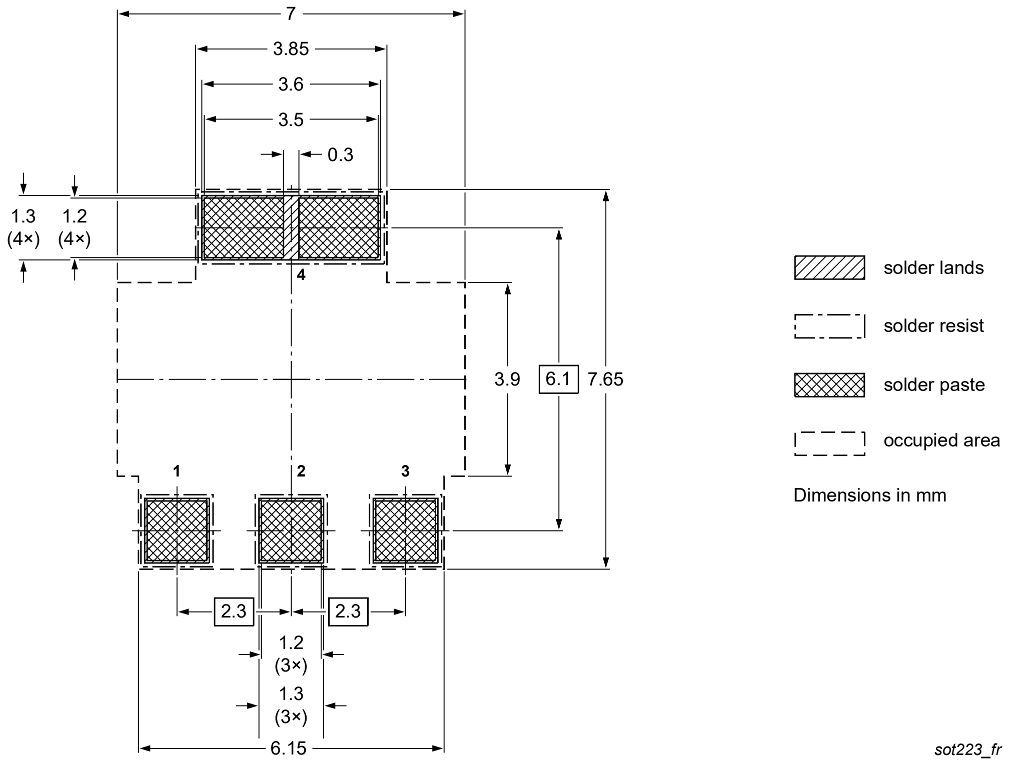


Fig. 20. Reflow soldering footprint for SC-73 (SOT223)

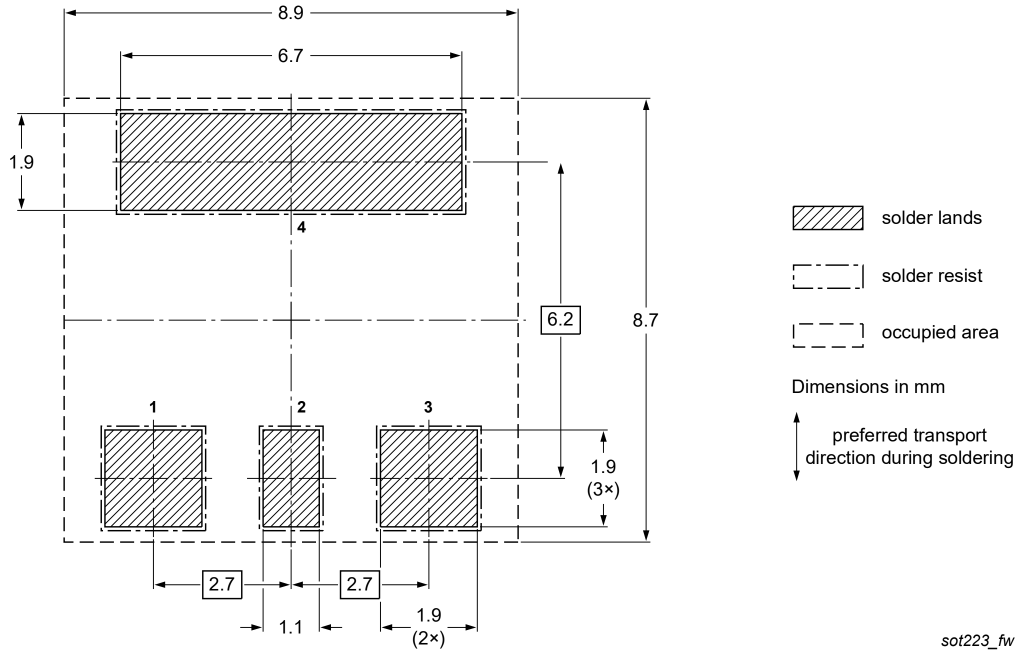


Fig. 21. Wave soldering footprint for SC-73 (SOT223)

14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PBSS4021PZ-Q v.2	20240920	Product data sheet	-	PBSS4021PZ-Q v.1
Modifications:	• New graphics added, graphs updated and values changed.			
PBSS4021PZ-Q v.1	20240212	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.
[2] The term 'short data sheet' is explained in section "Definitions".
[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at https://www.nexperia.com.

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Date of release: 20 September 2024